Learning Module With Digital Clock Networks Up Counter IC 74393 Using Proteus Software

Rudi Ernanto --- Sandra Husein

Abstract

The objective of this final project is the development of applications of digital basic techniques to improve the understanding of cadets in practice, the realization of the practice applicative modules digital clock built by the counter so that it can be used cadets majoring electronics as the development of digital basic techniques. The development method used is ranging from designing, building and testing circuits using the simulation on proteus software. Results of the development of this thesis came to the conclusion that The digital clock can be constructed using a circuit composed of IC-up counter. Digital clock learning modules created to provide knowledge of the basic techniques of digital applications. The design of the final project is done in several steps: the making up counter 9 digits, manufacture up to 60 counters and counters manufacture up to 24. After each part of the design above can be run with either the merging process is carried out so as to obtain the unity of the working complete. Work can provide a complete simulation function resembles a digital clock digital clock on properly.

Keywords: IC 74393, IC 7447, Up Counter, Seven Segment